

PRODUKT-INFO

VCXO-705CC

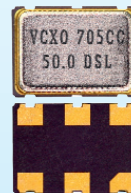
SMD Voltage Controlled Crystal Oscillators

FEATURES

1. Miniature package.
2. CMOS output.
3. Tri-State enable/disable.
4. Industry standard.
5. 3.3V operation.

APPLICATION

For use in phase locked loops, phase shift keying and other telecommunication applications such as ADSL, Cable modem, etc.



Actual Size



ELECTRICAL SPECIFICATIONS		
Model	Condition	VCXO-705CC
Frequency Range		50 MHz ~ 200 MHz
Frequency Calibration	at +25°C	±15 ppm
Temperature Stability	Over Operating Temperature	±15 ppm / ±25 ppm / ±50 ppm
Stability vs Power Change	V _{DD} ±5%	±5 ppm
Stability vs Load Change	15 pF ±10%	±3 ppm
Pullability	Over Control Voltage Range	±50 ppm / ±100 ppm / ±200 ppm
Control Voltage Range		0 ~ 3.3V
Operating Temperature Range		0°C to +70°C, -40°C to +85°C option
Storage Temperature Range		-55°C to +125°C
Supply Voltage (V _{DD})		3.3V ±5%
Supply Current	50.0 ~ 95.999 MHz 96.0 ~ 200.0 MHz	30 mA max. 40 mA max.
Output Symmetry	at 1/2 V _{DD}	40 ~ 60%, 45 ~ 55% option
Output Voltage	V _{OH} V _{OL}	90% V _{DD} min. 10% V _{DD} max.
Rise Time	20% V _{DD} ~ 80% V _{DD}	1 ns max.
Fall Time	80% V _{DD} ~ 20% V _{DD}	1 ns max.
Output Load		15 pF max.
Start Time		10 ms max.
Pin 2, Tri-State Function		Pin 2=H or open ... output active at pin 4,5 Pin 2=L ...high impedance at pin 4,5
Aging first year	at +25°C ±3°C	±2 ppm / year max.

OPTIONS: A 0.01 uF bypass capacitor should be placed between V_{DD} (pin 6) and GND (pin 3) to minimize power supply line noise.

Dimensions (Units: mm)

7.00 ± 0.10
5.00 ± 0.10
1.80 MAX

MARKING AREA

Top View

1.40 2.54 2.54 0.60
1.27

Recommended Solder Pattern

2.54 0.74 1.80
1.80 2.20

Test Circuit

V_{DD} V_{control} INH Test Point
0.01uF **CL=15pF

**Includes stray and probe capacitance

Output Waveform

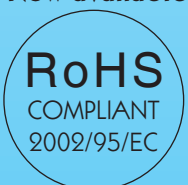
V_{OH} V_{OL} 80%V_{DD} 50%V_{DD} 20%V_{DD} GND
T₁ T₀=1/F₀ Symmetry=T₁/T₀x100%

7.0 x 5.0 x 1.8

Terminal	Connection
#1	V Control
#2	Tri-State
#3	GND
#4	OUTPUT
#5	N.C. (Tri-State)
#6	V _{DD}

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