

PRODUKT-INFO

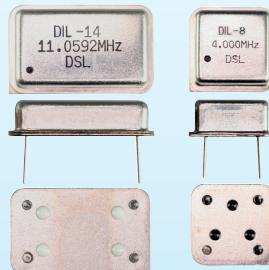
DIL-14 • DIL-8 OSCILLATOR HXO SERIES

FEATURES

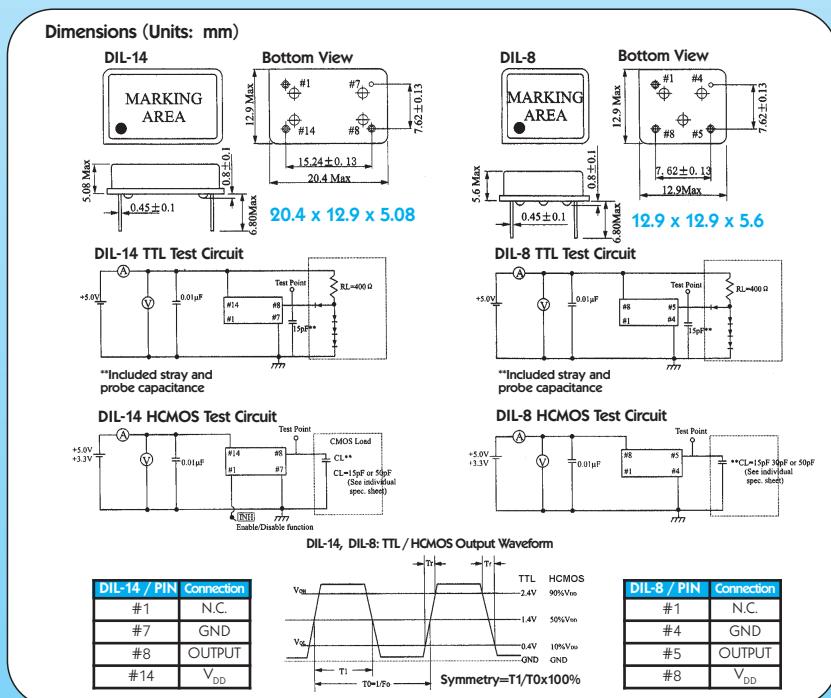
1. Full metal resistance welded package and pin 7 case ground minimize the RF radiation and EMI.
2. Standard full size and half size 4 pin DIP package. Glass stand-offs at bottom for proper defluxing.
3. TTL/CMOS output. Choice of ± 25 ppm, ± 50 ppm and ± 100 ppm inclusive frequency stability.

ELECTRICAL SPECIFICATIONS $T_A = 25^\circ\text{C}$	
Model	DIL-14 • DIL-8
Frequency Range	0.1 MHz ~ 150.0 MHz
Input Voltage	5 V $\pm 10\%$, 3.3 V $\pm 10\%$
Package Designator	DIL-14: Full size 4 pin DIP package DIL-8: Half size 4 pin DIP package
Frequency Stability Designator*	"A": ± 25 ppm ($\pm 0.0025\%$) "B": ± 50 ppm ($\pm 0.005\%$) "C": ± 100 ppm ($\pm 0.01\%$) default and standard
Output Load	50 pF or 10 TTL Gates
Input Current	3 – 80 mA, frequency dependent
Logic „1“ Level	90% V_{DD} min.
Logic „0“ Level	10% V_{DD} max.
Symmetry (Duty Cycle)	50% $\pm 10\%$ max. at 0.5 V_{DD} default and standard 50% $\pm 5\%$ max. at 0.5 V_{DD} option
Rise and Fall time	4 ns max. at 20% ~ 80% V_{DD}
Operating Temperature	0°C to +70°C, -40°C to +85°C
Option on Pin 1**	Tri-State function: enable / disable
Disable Phase Delay	100 ns max.
Storage Temperature	-50°C to +125°C

Actual Size



OPTIONS: *Inclusive of $+25^\circ\text{C}$ tolerance, operating temperature range, $\pm 10\%$ input voltage variation, load change, aging, shock and vibration. **When „0“ ($\leq 0.8\text{V}$) is applied to the pin 1 the output becomes high impedance. The output becomes active when „0“ level is removed.



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All specifications subject to change without notice.