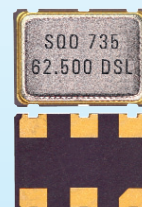


# PRODUKT-INFO

## VC-SQO-735/LV-PECL • VC-SQO-735/LVDS VC-SQO-735/HCMOS • SMD OSCILLATOR

### FEATURES

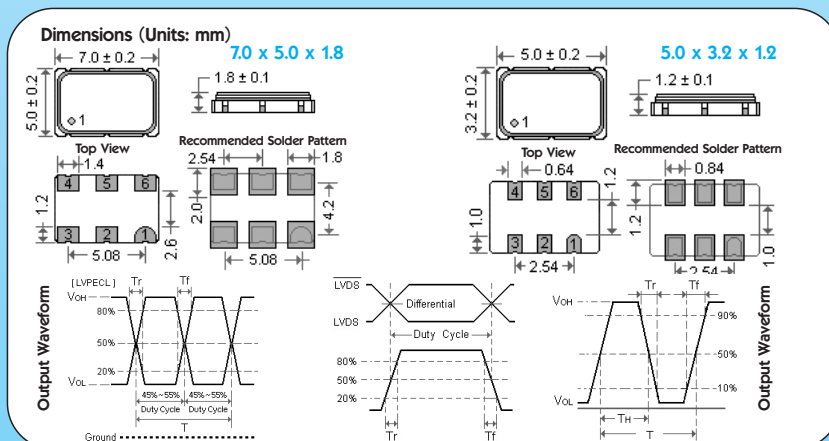
1. Ultra miniature package clock oscillator with size of 5.0 x 3.2 x 1.2 mm and 7.0 x 5.1 x 1.8 mm.
2. Supply Voltage: 3.3 V and 2.5 V.
3. Differential output (LV-PECL, LVDS, HCMOS).



Actual Size



ELECTRICAL SPECIFICATIONS			
Model	VC-SQO-735/LV-PECL	VC-SQO-735/LVDS	VC-SQO-735/HCMOS
Frequency Range	10.0 MHz ~ 1450.0 MHz		10.0 MHz ~ 245.0 MHz
Frequency Stability	±25 ppm / ±50 ppm / ±100 ppm		
Operating Temperature Range	-10°C to +70°C, -40°C to +85°C option		
Storage Temperature Range	-55°C to +150°C		
Supply Voltage	2.5 V ±5% / 3.3 V ±5%		
Current with Output	16 mA typical		
Current Consumption, 2.5 V	100 MHz: 46mA 250 MHz: 48 mA 500 MHz: 53 mA	750 MHz: 56 mA 1.0 GHz: 60 mA 1.35 GHz: 65 mA	100 MHz: 16mA 250 MHz: 18 mA 500 MHz: 21 mA
Current Consumption, 3.3 V	100 MHz: 50mA 250 MHz: 55 mA 500 MHz: 60 mA	750 MHz: 64 mA 1.0 GHz: 68 mA 1.35 GHz: 72 mA	100 MHz: 25mA 250 MHz: 30 mA 500 MHz: 35 mA
Output Specification	LV-PECL	LVDS	HCMOS
Output Load	Differential	Differential	15 pF
Output Logic	„0“ VOL „1“ VOH	V <sub>DD</sub> 1.1 typ, V <sub>DD</sub> 0.9 max. V <sub>DD</sub> 1.4 typ, V <sub>DD</sub> 1.6 max.	10% V <sub>DD</sub> 90% V <sub>DD</sub>
Rise Time /Fall Time	0.2 ns typical 0.5 ns max.	0.2 ns typical 0.4 ns max.	1.5 ns typical 3.0 ns max.
Output Symmetry	50% ±5%		
Integrated Phase Jitter (RMS), 12kHz ~ 20MHz	0.6 ps typical		
Start Time	10 ms max.		
Aging (at +25°C) first year	±2 ppm / year max.		



Terminal	LV-PECL / LVDS / HCMOS
#1	Control Voltage
#2	Enable/Disable
#3	GND
#4	HCMOS: OUTPUT LV-PECL/LVDS: Differential
#5	HCMOS: N.C. LV-PECL/LVDS: Complementary
#6	V <sub>DD</sub>

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All specifications subject to change without notice.