

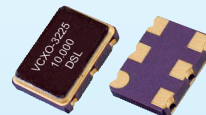
# PRODUKT-INFO

## VCXO-3225

### SMD Voltage Controlled Crystal Oscillators

#### FEATURES

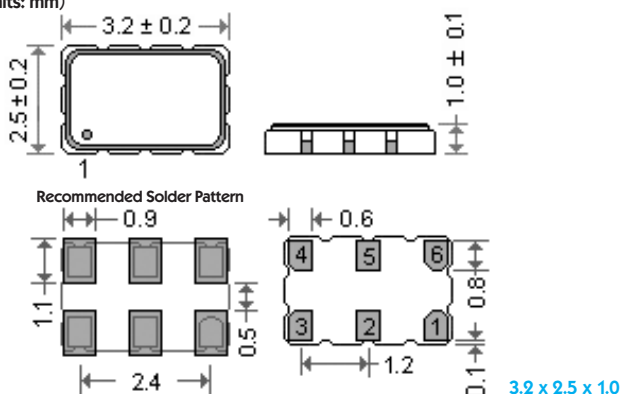
1. Supply Voltage: 5.0 V, 3.3 V, 2.5 V and 1.8 V.
2. Output: TTL/CMOS.



ELECTRICAL SPECIFICATIONS				
Model	Condition	VCXO-3225		
Frequency Range		1.0 MHz ~ 50.0 MHz	6.0 MHz ~ 50.0 MHz	
Frequency Stability		±25 ppm / ±50 ppm / ±100 ppm		
Operating Temperature Range		-10°C to +70°C, -40°C to +85°C option		
Input Impedance		1 MΩ typical		
Power Supply Voltage		5.0 V±5%	3.3 V±5%	2.5 V±5% 1.8 V±5%
Current Consumption		10 ~ 45 mA frequency dependent		
Initial Frequency Accuracy	at +25°C	to tune to the nominal frequency with		
		V <sub>c</sub> =2.5V ±0.2V	V <sub>c</sub> =1.65V±0.2V	V <sub>c</sub> =1.25V±0.2V V <sub>c</sub> =0.9V±0.2V
Output Logic	Level „1“	4.5 V min.	2.97 V min.	2.25 V min. 1.62 V min.
	Level „0“	0.5 V max.	0.33 V max.	0.25 V max. 0.183 V max.
Frequency Deviation Range	Standard	±80 ppm min.		
Control Voltage Center		2.5 V <sub>DC</sub>	1.65 V <sub>DC</sub>	1.25 V <sub>DC</sub> ±1 V 0.9 V <sub>DC</sub>
Control Voltage Range		0.5 V to 4.5 V	0.3 V to 3.0 V	0.25V to 2.25V 0 V to 1.8 V
Output Load		15 pF max.		
Start Time		5 ms typical, 10 ms max.		
Integrated Phase Jitter	12 kHz to 20 MHz	200 fsec typical, +3.3 V, 270 MHz		
Tri-State Control Characteristics		Tri-State enable high. No connection or V <sub>DD</sub> -0.5V min. is applied to Tri-Statepin to enable output.*		
Aging first year	at +25°C ±3°C	±3 ppm / year max.		

\* Ground +0.5V mx. to disable output (high impedance).

Dimensions (Units: mm)



Terminal	Connection
#1	V Control
#2	Enable/Disable
#3	GND
#4	OUTPUT
#5	N.C.
#6	V <sub>DD</sub>

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All specifications subject to change without notice.